Product Training Guide

# Plasma Display Systems
# PDP-5030HD & PDP-4330HD

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Pioneer

SERVICE

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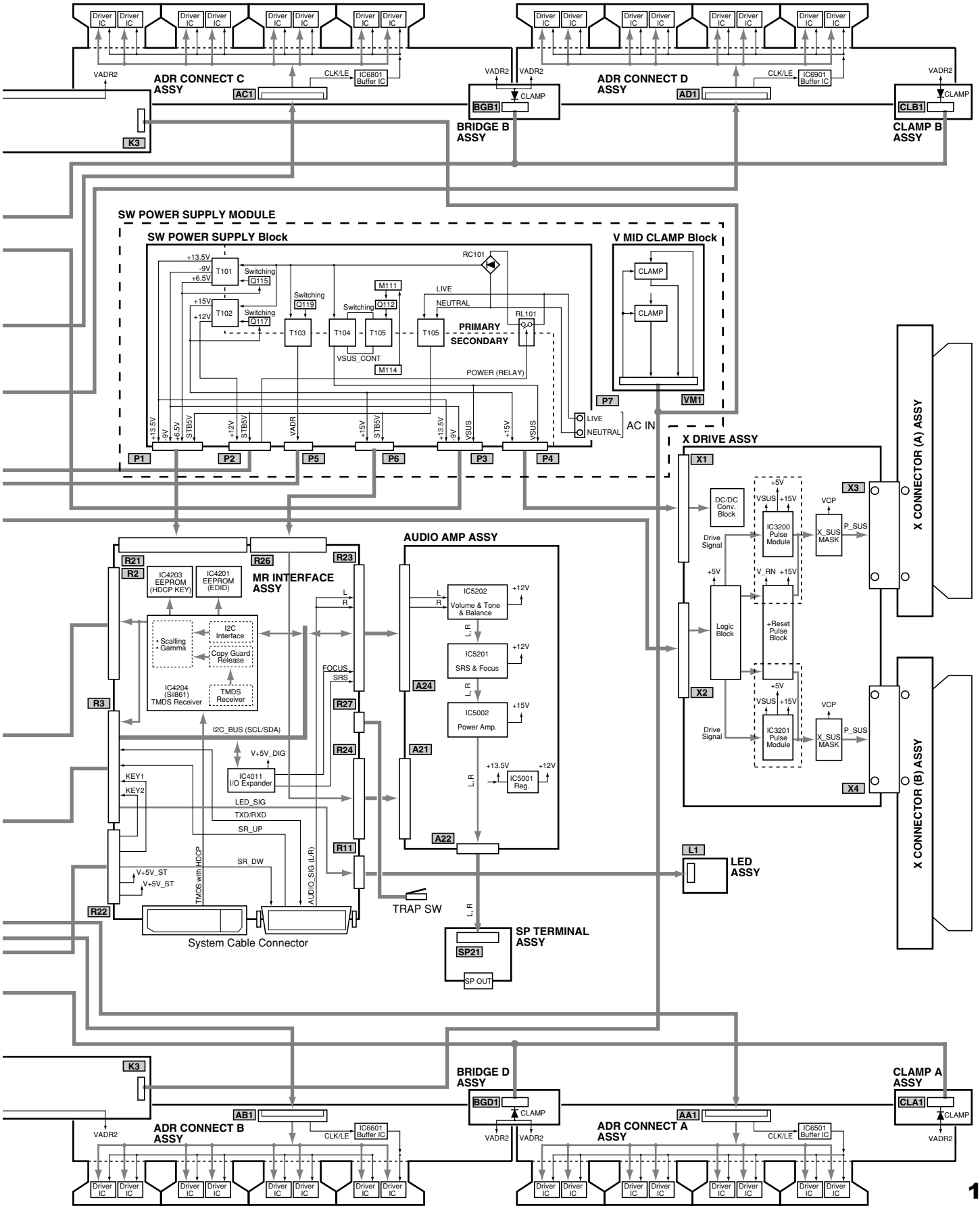
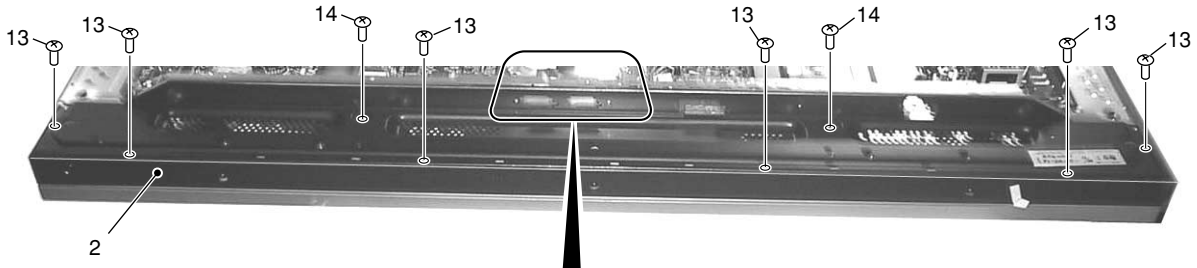
Note : When ordering service parts, be sure to refer to "EXPLODED VIEWS and PARTS LIST" or "PCB PARTS LIST".

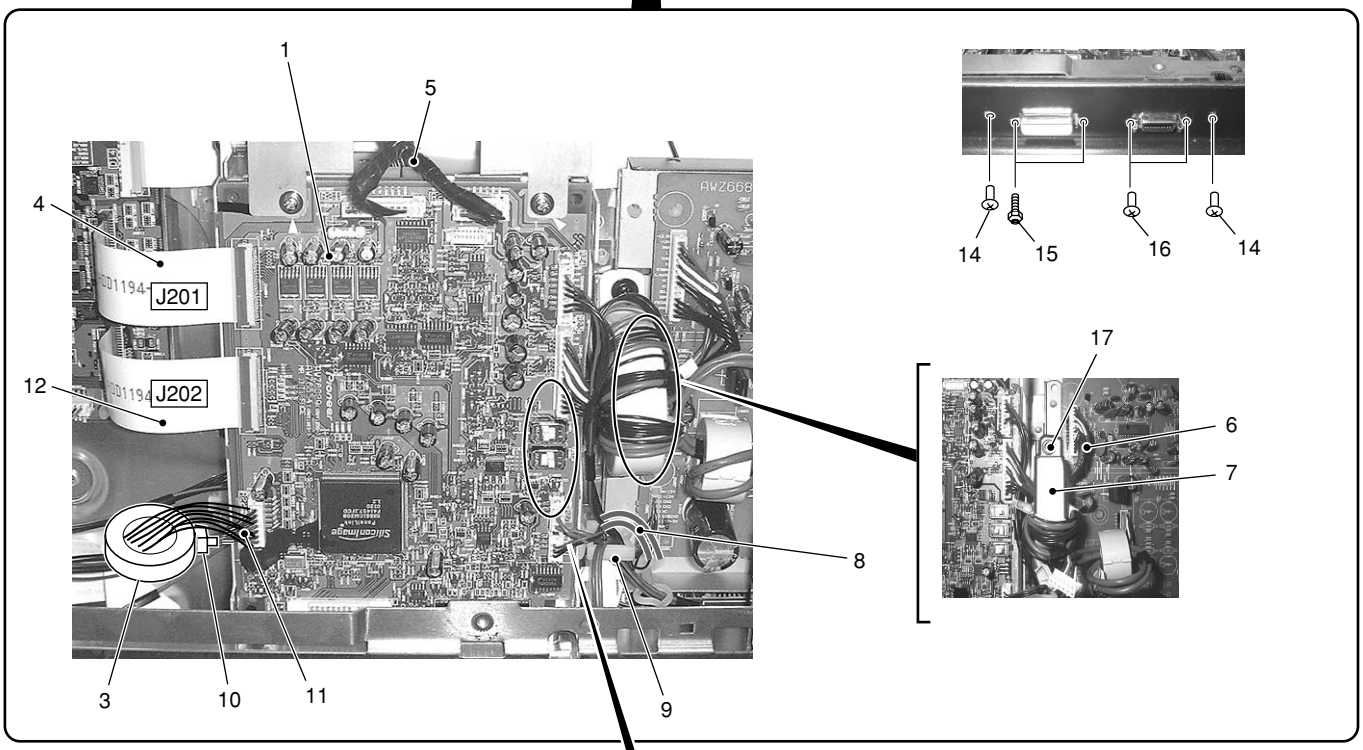


# Sub Address Block

**The two Sub Address Circuits are located just above and below the address driver connect assembly's ( between B & C connectors ). See the overall block Diagram for locations. The upper and lower sub address circuits monitor the current draw of the 60 volt supply to the address drivers. If a small amount of current draw is detected (more than normal) a signal is sent back to the Digital Video Assembly (ADRK EMG1) as an emergency signal and the Digital Video Assembly will limit the number of address data pulses in order to lower the current draw. If the current draw is too large the address power down signal will be sent to the Digital Video Assembly (ADRK PD) and the panel will go into power down condition. (red flashing LED).**

## UPPER LAYER SECTION (1)



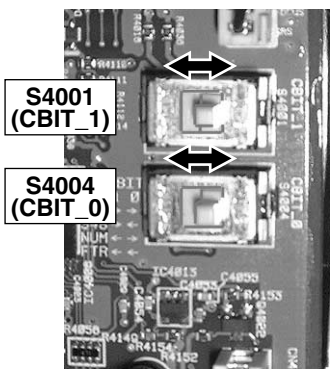




### Caution in the MR INTERFACE Assy Replacement

Set the slide switches in accordance with model chart when replacing the MR INTERFACE Assy.

| | **S4001** CBIT_1 | **S4004** CBIT_0 |
|---|---|---|
| PDP-4333P | → | → |
| PDP-433PE | ← | → |
| PDP-433PU | → | → |

Note 1: When there is not S4004, set only S4001.
Note 2: When there are not S4001 and S4004, setting is unnecessary.



## Sustain Pulse Waveform Adjustment

| Input Signal | Adjusting Point | Adjusting Method |
|---|---|---|
| White 100% | REF_DIG mode<br>in Factory mode<br>XSUSB ADJ<br>YSUSB ADJ | **X-SUS-B, Y-SUS-B Adjustment**<br>Set to the indicated value with the remote control unit.<br>(Refer to "Timing adjustment of control signal of X and Y Drive Assys".) |

## VRN Voltage Adjustment

| Input Signal | Adjusting Point | Adjusting Method |
|---|---|---|
| White 100% | VR3701 (VRN)<br>(X DRIVE Assy) | **VRN (minus reset voltage adjustment)**<br>Adjust so that the voltage between K3707 (VRN) and K3702 (SUS-GND) becomes<br>-280V ± 1.0V. |

## Panel White Balance Adjustment

| Input Signal | Adjusting Point | Adjusting Method |
|---|---|---|
| | | Adjust the parameter in the OFFSET-DIGITAL of factory mode as follows;<br>PANEL R-HIGH<br>〜<br>PANEL B-LOW<br>In this time, dispay uses the mask (MASK04) of factory mode.<br>Reference : Adjustment values using the Media color-difference meter (A-100) |

| | MASK Left Side | MASK Right Side |
|---|---|---|
| x | 294 | 293 |
| y | 303 | 294 |

* When performing the various adjustments by RS-232C control, execute a "DM0" command (release the limit of pulse number) beforehand.
  After the adjustment completion, execute a "DM 3" command (Limit of pulse number: 64%, shipping state)

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